



Vtarget = High, TP1 = +5V  
 Vtarget = Low, TP1 = +6.5V

P3.3 = High, TP2 = +5V  
 P3.3 = Low, TP2 = +12.5V

Note: 1. A15-A16, RTS, CTS are not implemented in this design.  
 They are reserved for FLASH/PROM programmer.

Wichit Sirichote 26-08-2545		
Title	Easy-Downloader V3.0 89C51/89C52/89C55WD	
Size	Document Number	Rev 1
B	<Doc>	
Date:	Monday, August 26, 2002	Sheet 1 of 1