



8255 Memory mapped I/O  
 PORTA = 0x000  
 PORTB = 0x001  
 PORTC = 0x002  
 CONTROL PORT = 0x003

Microprocessors Lab, Dept. of Applied Physics, KMITL		
Title C52 EVB V2.0, external eeprom boot loader		
Size B	Document Number 001	Rev 1
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